

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

**Applicant:** TAKAO HONDA

**Docket:** 17454

**Serial No:** unassigned

**Art Unit:** unassigned

**Filed:** herewith

**Dated:** February 18, 2004

**For:** SEMICONDUCTOR INTEGRATED  
CIRCUIT DEVICE

Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

**INFORMATION DISCLOSURE STATEMENT**

Sir:

In accordance with 37 C.F.R. §§ 1.97 and 1.98, it is requested that the following reference, which is also listed on the attached Form PTO-1449, be made of record in the above-identified case.

1. Japanese Patent Kokai Publication No. 8-181596 dated July 12, 1996.


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Dated: February 18, 2004

  
\_\_\_\_\_  
Paul G. Esatto, Jr.

Applicant is submitting a copy of the above-cited reference. The relevance of the above-identified reference has been described in the specification.

Inasmuch as this Information Disclosure Statement is being submitted in accordance with the schedule set out in 37 C.F.R. § 1.97(b), no statement or fee is required.

Respectfully submitted,



Paul J. Esatto, Jr.  
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PJE:eg

<b>INFORMATION DISCLOSURE CITATION</b> <i>(Use several sheets if necessary)</i>	Docket Number (Optional) <b>17454</b>	Application Number <b>unassigned</b>
	Applicant(s) <b>TAKAO HONDA</b>	
	Filing Date <b>Herewith</b>	Group Art Unit <b>unassigned</b>

**U.S. PATENT DOCUMENTS**

*EXAMINER INITIAL	REF	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE

**FOREIGN PATENT DOCUMENTS**

	REF	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	Translation	
							YES	NO
		8-181596	7/12/97	Japan				

**OTHER DOCUMENTS** *(Including Author, Title, Date, Pertinent Pages, Etc.)*


EXAMINER	DATE CONSIDERED
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**EXAMINER:** Initial if citation considered, whether or not citation is in conformance with MPEP Section 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.